Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	48061	@ad<"20030626" and (speed frequency) same match	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/29 09:40
L2	117	@ad<"20030626" and (speed frequency protocol) same match same PCI\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/29 09:40
L3	2	L2 and server and backplane	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/29 09:40
S1	2	"20040003155"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:28
S3	2	@ad<"20030626" and ("714"/\$. ccls. "710"/\$.ccls.) and incompatibility same PCI\$2 same speed	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 16:44
S4	3176	@ad<"20030626" and (printed adj circuit adj board PCB) and backplane	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:32
S5	0	S4 and operating adj incompatibility	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:32
S6	1593	S4 and speed	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:32
S7	261	S6 and PCI	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:32

S8	0	S7 and speed with conflict	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:33
S9	0	S7 and operating same incompatiblity	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:33
S10	97	S7 and ("710"/\$ "714"/\$)".ccls"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:35
S11	1	S10 and incompatibility	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:35
S12	0	S10 and (board card) with speed with detect\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:35
S13	29	@ad<"20030626" and ("714"/\$. ccls. "710"/\$.ccls.) and (board card) with \$2compati\$6 with speed	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:37
S14	162	@ad<"20030626" and (board card) with \$2compati\$6 with speed	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:37
S15	31	S14 and PCI	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:37
S16	31	S14 and PCI\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:41
S17	3	S16 and backplane	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:37

S18	30	@ad<"20030626" and (board card) same speed with match\$3 same PCI	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 10:58
S19	27	@ad<"20030626" and (board card) same speed with match\$3 same PCI same \$2compatib\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 13:16
S20	10	("5,657,455" "6,754,747" "6,820, 156" "6,134,621" "6,484,222" "5, 657,455").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 14:36
S21	5	("5930496"   "6081143"   "6189058"   "6272582"   "6295568").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/28 11:46
S22	0	10/606872	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/28 11:46
S23	2	"10606872"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/28 12:31
S24	1	"6484222".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/28 12:31
S25	1	S24 and RESET	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/28 12:31
S26	27	@ad<"20030626" and (board card) same (frequency speed) with match\$3 same PCI same \$2compatib\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 14:36
S27	27	@ad<"20030626" and (board card) same (frequency speed) with match\$3 same PCI same \$2compatib\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 14:36
S28	0	S27 and boot	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 14:36

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S29	10	("5,657,455" "6,754,747" "6,820, 156" "6,134,621" "6,484,222" "5, 657,455").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 14:36
S30	0	S29 and boot	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 14:36
S31	0	S29 and start	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 14:36
S32	7	S29 and PCI	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 14:36
S33	1	S32 and boot\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 14:37
S34	10	("5,657,455" "6,754,747" "6,820, 156" "6,134,621" "6,484,222" "5, 657,455").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 16:44
S35	422	@ad<"20030626" and ("714"/\$. ccls. "710"/\$.ccls.) and bus same (speed frequency) and (first with board) and (second with board)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:15
S36	123	S35 and backplane	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 16:45
S37	49	S36 and (first with circuit adj board) same (second with circuit adj board)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 16:46
S38	14	S37 and decoder	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 16:46

6/29/06 10:25:40 AM

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S39	37	@ad<"20030626" and 710/16. ccls. and PCI\$2 and (speed frequency)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:12
S40	14	@ad<"20030626" and 710/16. ccls. and PCI\$2 same (speed frequency)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:12
S41	6	S40 and server	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:12
S42	14	S39 and server	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:12
S43	0	@ad<"20030626" and PCI and server and backplane and characteristic same determination	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:33
S44	37	@ad<"20030626" and PCI and server and backplane and characteristic same (speed frequence protocol)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:16
S45	325	@ad<"20030626" and PCI and server and backplane and (identif\$7 characteristic) same (speed frequence protocol)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:17
S46	104	S45 and (PCB circuit adj board\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:18
S47	327	@ad<"20030626" and PCI\$2 and server and backplane and (identif\$7 characteristic) same (speed frequence protocol)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:18
S48	104	S47 and (PCB circuit adj board\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:20

S49	13	S48 and "710"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:20
S50	0	@ad<"20030626" and PCI same match same PCB same (speed frequency)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:34
S51	78	@ad<"20030626" and PCI\$2 same match same (speed frequency)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:36
S52	7	S51 and server	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:34
S53	27	@ad<"20030626" and PCI\$2 same detect\$3 same match same (speed frequency)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/29 09:39
S54	51	S51 NOT S53	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:41
S55	4	S54 and backplane	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/28 17:41